

**Final Call for Papers**  
**The 2015 International Conference on**  
**Field Programmable Technology (FPT 2015)**



FPT is the premier conference in the Asia-Pacific region on field-programmable technologies including reconfigurable computing devices and systems containing such components. Field-programmable devices promise the flexibility of software with the performance of hardware. The development and application of field-programmable technology have become important topics of research and development. Field-programmable technology is widely applied, in high-performance computing systems, embedded and low-power control instruments, mobile communications, rapid prototyping and product emulation, among other areas.

**Scope:** Original contributions are sought in the area of field programmable technologies and applications:

- **Tools and design techniques** for field-programmable technology: placement, routing, synthesis, verification, debugging, run-time support, technology mapping, partitioning, parallelization, timing optimization, design and run-time environments, languages and modelling techniques, provably-correct development, intellectual property core-based design, domain-specific development, hardware/software co-design.
- **Architectures** for field-programmable technology: field-programmable gate arrays, complex programmable logic devices, coarse-grained reconfigurable arrays, interconnect, analogue arrays, arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, high-performance reconfigurable systems, evolvable hardware and adaptive computing, fault tolerance and avoidance.
- **Device technology** for field-programmable technology: programmable memories such as non-volatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, and emerging VLSI device technologies.
- **Applications** of field-programmable technology: biomedical and scientific computation accelerators, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, evolvable and biologically-inspired hardware, financial application, big data management, aerospace and extreme environment applications.
- **Education** for field-programmable technology: courses, teaching and training experience, experiment equipment, design and applications.

**Submission of papers:** Prospective authors are invited to submit papers describing original research or high quality tutorial expositions for double-blind review. Full papers should not exceed 8 pages; papers associated with poster presentation, demonstration session, design competition or PhD forum should not exceed 4 pages. Accepted papers presented at the conference will be submitted for inclusion in IEEE Xplore. In submitting a paper, you warrant that at least one of the authors will register and present the paper at the conference. Failure to present will result in the paper not being submitted for inclusion on IEEE Xplore.

<b>Submission deadlines:</b>	Technical papers (full and poster)	<b>ABSTRACT</b>	<b>19 July, 2015</b>
		<b>Firm paper submission deadline</b>	<b>EXTENDED TO 24 July, 2015</b>
	Notification of acceptance		<b>7 September, 2015</b>
	Demo, PhD forum and design competition papers		<b>20 September, 2015</b>
	Notification of acceptance		<b>12 October, 2015</b>
	Final camera ready copy		<b>26 October, 2015</b>

**Further information:** Conference website: <http://fpt.massey.ac.nz>  
Contact the chairs: [ICFPT@massey.ac.nz](mailto:ICFPT@massey.ac.nz)